



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **HAEMATSU, Hitoshi**

Group Art Unit: **2822**

Serial No.: **10/659,337**

Examiner: **Pamela E. PERKINS**

Filed: **September 11, 2003**

P.T.O. Confirmation No.: 7094

For. **SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF**

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: January 3, 2006

Sir:

This Preliminary Amendment is being filed concurrently with a Request for Continued Examination for the above-captioned patent application. Prior to further examination, please amend the above-captioned patent application as follows: